

<p>Form 1449/PTO</p> <p>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</p> <p>(use as many sheets as necessary)</p>				<i>Complete if Known</i>	
Application Number	Not Yet Assigned <u>10/782,150</u>				
Filing Date	November 4, 2003				
First Named Inventor	K.B. Desai				
Art Unit	Not Yet Assigned <u>2863</u>				
Examiner Name	Not Yet Assigned				
Page	1	of	2	Attorney Docket Number	P8966

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS

Examiner Initials	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T
VXH		COMER, D. J. "Chapter 6: Introduction to State Machines; Chapter 10: Asynchronous State Machines" In <i>Digital Logic and State Machine Design</i> . Saunders College Publishing: Philadelphia, etc., 2 nd Edition, 1990, pp. i-xii, 239-261, 455-475.	
VXH		TAUB, H. and DONALD SCHILLING. "Preface, Chapter 3: Logic Circuits; and Chapter 6: Asynchronous State Machines," In <i>Digital Integrated Electronics</i> .. McGraw-Hill Electrical and Electronic Engineering Series. McGraw-Hill Book Co.: New York, 1977, 1 st 5 pages unnumbered, ix-x, 8-133.	

Examiner Signature	thien Vo	Date Considered	3/14/05
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MP&P § 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.